Dr. SHERAZ ANJUM *(MSc PhD MIET RE)*

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Qualification

- **PhD Engineering (July 2008)**
  Title of Thesis: *Design Modeling and Performance Evaluation of NoC Architectures and Algorithms*
  University: *Institute of Microelectronics, Graduate University of Chinese Academy of Sciences, Beijing, China*
  Area of Interest: *Networks on Chip, Multi-Processor System on Chip, Machine Learning, Wired and Wireless Networks, IoT, Intelligent Transportation System, Computer Vision and Computer Architectures*
- **M. Sc Computer Engineering (2005)**
  Title of Thesis: *Partial Reconfiguration of Virtex FPGAs using Run Time Parameterizable Cores for Communication Algorithms*
  University: *Center of Advanced Studies in Engineering, University of Engineering and Technology, Taxila, Pakistan*
- **M. Sc Electronics (1999)**
  University: *Quaid-E-Azam University, Islamabad, Pakistan*

Distinctions

- Registered with Pakistan Engineering Council
- Approved PhD Supervisor from HEC (Higher Education Commission)
- 26 years of Teaching and Research experience including 15 years at Post PhD graduate level
- Session Chair in 24th IEEE International Multi Topic Conference October 21-22, 2022 at FAST-NU.
- Member TPC and Session Chair in 22nd IEEE International Multi Topic Conference 2019
- As Facilitator IET on Campus CUI Wah Network managed the PATW (Present Around the World) Stage- I Competition held on April 18, 2019 at COMSATS University Islamabad, Wah Campus.
- Event Chair and External Judge of IET UK's Stage-1 PATW Competition held at NUST SMME Islamabad on 23rd April 2018.
- Event Chair and External Judge of IET UK's Stage-1 PATW Competition held at IST Islamabad on 18th April 2018.
- Event Chair and Judge of IET UK's Stage-2 PATW Competition held at Envoy Continental Islamabad on 6th May 2017.
- Event chair and External Judge of IET UK's Stage-1 PATW Competition held at IST Islamabad on 28th April 2017.
- A startup research grant project of 5 Lakh titled "Real Time Implementation of Shared Memory Multiprocessor Systems on Chip" approved by Higher Education Commission of Pakistan completed in June 2016 as Co-PI.
- Invited as judge for four consecutive years in COMPEC 2011 to COMPEC 2014 organized by Department of Computer Engineering, College of E&ME, NUST Rawalpindi
- Reviewer of Journal of System Simulation, Journal of System Architecture, Computers and Electrical Engineering, Journal of Computer Science and Technology and Microprocessors and Microsystems.
- Reviewer of Frontiers of Information Technology from 2010 to 2019
- Supervised many undergraduate final year project

MS/PhD Thesis Supervision

Sr. No.	Student Name	MS/PhD	Thesis Title	Status
1	Muhammad Imran	PhD	Statistical delay QoS driven analysis of NOMA for 5G and beyond	In Progress
2	Eisha Saeed	MS	Exploring Deep Learning and Sentence Embeddings for Detection of AI Generated Content	In Progress
3	Faiz Bahu	MS	Detection of Fake News using LLM for Urdu Dataset	Completed in Fall 2024
2	Sultan Mouzim Ali Hamdani	MS	Enhancing Gastrointestinal Disease Diagnosis Using Transformer-Based Computer Vision	Completed in Fall 2023
3	Waqar Amin	PhD	Optimized Application Mapping Framework For Network on Chip	Completed in Spring 2023
4	Syed Najam UL Hassan Naqvi	MS	Edge Detection and LSB-Based Image Steganography Technique	Completed in Fall 2022
5	Rabbia Shakeel	MS	Predicting Radio and Queuing Losses in Wireless Sensor Networks	Completed in Spring 2022
6	Zahra Saleem	MS	Blockchain-Based Trusted Scheme Using Steganography in Drone-Based VANET	Completed in Spring 2022
7	Sonaina Sheikh	MS	Stance Classification in Social Media using Linguistic and Emotion-Based Features	Completed in Fall 2019
8	Sarzamin Khan	PhD	Design and Analysis of Low Energy and Computationally Efficient IP Mapping Algorithms for Network-on-Chip	Completed in Fall 2018
9	Zulqar Nain	MS	Novel Fault Tolerant Routing Scheme for 2D Mesh based on-chip Architectures	Completed in Spring 2018
10	Muhammad Kashif Akram	MS	An Adaptive and Dynamic Fault Tolerant Routing Algorithm for NoC	Completed in Spring 2018
11	Hafiz Ameer Hamza	MS	Classification of Stomach Infections based on Multiple Features Extraction and SVM Classification	Completed in Spring 2018
12	Usman Ali Gulzari	PhD	Design and Analysis of Optimized Router Architecture and Scalable Network Topology for On Chip Communication	Completed in Fall 2016
13	Saima Iqbal	MS	Comparative Analysis of PCBs with Embedded Resistors Vs PCBs with SMD Components	Completed in Spring 2013

MS/PhD Thesis Evaluation as External Examiner

Sr #	Student Name	MS/ PhD	Thesis Title	Department/University/Year
1	Muhammad Ibrahim	MS	A Hybrid Multilayer Approach for Detection and Correction of Transient Faults in NoC Router for Chip Multiprocessors	Department Of Computer Engineering, UET Taxila, Pakistan in Spring 2019
2	Muhammad Zeeshan	PhD	Adaptive High Throughput CDMA-based Wideband Waveform with Robust Timing and Frequency Synchronization for SDR-based Tactical Networks	Department of Electrical Engineering, College of EME, NUST, Pakistan in Spring 2015
3	Ayman Bukhari	MS	A Multiplierless Implementation of a Farrow Structure based on Multiple Constant Multiplications Technique	Department of Electrical Engineering, NUCES Islamabad, Pakistan in Spring 2015
4	Hafiz Allah Nawaz	MS	Modeling and Performance Analysis of an Adaptive Calibration Structure for an M-periodic Time-Varying System	Department of Electrical Engineering, NUCES Islamabad, Pakistan in Spring 2013
5	Muhammad Umer	MS	Non-Coherent Detection Performance of a Direct Sequence Time Hopping Code Division Multiple Access System	Department of Electrical Engineering, NUCES Islamabad, Pakistan in Spring 2013

MS/PhD Thesis Evaluation as Internal Examiner

Sr #	Student Name	MS/ PhD	Thesis Title	Department/University/Year
1	Muhammad Ateeq	PhD	Multi-Parametric Analysis of QoS in Wireless Sensor Networks: A Data Driven Approach	Department of Computer Science COMSATS University Islamabad Wah Campus in Fall 2019
2	Jan Badshah	PhD	Efficient Cache Placement to Optimize Access Time in SDN-based ICN	Department of Computer Science COMSATS University Islamabad Wah Campus in Fall 2019
3	Ghulam Jillani Ansari	PhD	Natural Scene Text Understanding using Machine Learning	Department of Computer Science COMSATS University Islamabad Wah Campus in Spring 2019
4	Javaria Amin	PhD	Detection of Brain Tumor using MRI Based on Machine Learning	Department of Computer Science COMSATS University Islamabad Wah Campus in Spring 2019

Professional Registration

Sr #	Professional Body	Registered As	Registration No.
1	Pakistan Engineering Council	Registered Engineer (RE)	ELECTRO/23964
2	Institute of Engineering and Technology UK	Member of the Institute of Engineering and Technology (MIET)	1100353177

Awards/Certificates

- Completed a Course Certificate of "Supervised Machine Learning Regression and Classification" an online non-credit course authorized by DeepLearning.AI and offered through Coursera in September 2023.
- Completed a Course Certificate of "AI For Everyone" an online non-credit course authorized by DeepLearning.AI and offered through Coursera in August 2023.
- Completed a Course Certificate of "Neural Networks and Deep Learning" an online non-credit course authorized by DeepLearning.AI and offered through Coursera in October 2022.
- Completed a Course Certificate of "Python Programming: A Concise Introduction" an online non-credit course authorized by Wesleyan University offered through Coursera in December 2021.
- Completed a Course Certificate of "Introduction to Data Science in Python" an online non-credit course authorized by University of Michigan offered through Coursera in November 2021.
- Completed a Course Certificate of "Machine Learning" an online non-credit course authorized by Stanford University and offered through Coursera in October 2021.
- Certificate of Professional Development in "Testing and Assessment" held from 2nd April to 4th April 2019 at Faculty Development Academy, COMSATS University Islamabad
- Awarded Commendation Certificate by COMSATS University Islamabad on 30th March 2019 for successfully supervising PhD studies of Dr. Sarzamin Khan in the field of Electrical Engineering
- Awarded Certificate of recognition for Research Productivity by COMSATS Institute of Information Technology in 2010, 2011, 2012, 2013, 2015, 2016, and 2017
- Awarded Equivalence Certificate for PhD Engineering (Microelectronics and Solid-State Electronics) by HEC in September 2010
- Awarded PhD Scholarship by COMSATS in 2005
- Certificate of "JAVA Training Program" held from 30th April, 2001 to 30th July, 2001
- Certificate of attending a course in "Solaris System Administration" held in May 8-11, 2001

Technical Presentations

- Presented departmental report to the NCEAC AIC team reaccreditation visit for BSCS and BSSE degree programs of Department of Computer Science, COMSATS University Islamabad, Wah on Thursday February 20, 2025
- Presented departmental report to the NCEAC evaluator Dr Naveed Ikram, Riphah Intl. University, Islamabad for Zero Visit for BS Artificial Intelligence degree program of COMSATS University Islamabad, Wah on Friday, July 21, 2023
- Presented Department Report to National Computing Education Accreditation Council evaluation team on February 27, 2023.
- Delivered a talk at National C&IT Symposium held on 27-28 June, 2012 organized by Department of Computer Engineering, College of E&ME, NUST Rawalpindi, Pakistan
- Presented Vision 2020 on 2 July, 2012 for the Department of Electrical Engineering, COMSATS Institute of Information Technology Wah Cantt Campus, Pakistan.

Professional Experience

- **COMSATS University Islamabad Wah Cantt Campus**
  (Professor / Head Department of Computer Science, May 2023 – To Date)
  - ➢ Teaching to undergraduate and graduate levels
  - ➢ Supervision of graduate theses
  - ➢ Supervision of undergraduate projects
  - ➢ Convener Departmental Academic Review Committee
  - ➢ Member Board of Studies
  - ➢ Member Industrial Advisory Board
  - ➢ Member Short Listing and Selection Committee
  - ➢ Member Departmental Advisory Committee
- **COMSATS University Islamabad Wah Cantt Campus**
  (Associate Professor / Head Department of Computer Science, August 2022 – May 2023)
  - ➢ Teaching to undergraduate and graduate levels
  - ➢ Supervision of graduate theses
  - ➢ Supervision of undergraduate projects
  - ➢ Convener Departmental Academic Review Committee
  - ➢ Member Board of Studies
  - ➢ Member Industrial Advisory Board
  - ➢ Member Short Listing and Selection Committee
  - ➢ Member Departmental Advisory Committee
- **CIIT Wah Cantt (Associate Professor, June 2012 – July 2022)**
  - ➢ Teaching to undergraduate and graduate levels
  - ➢ Supervision of graduate theses
  - ➢ Supervision of undergraduate projects
  - ➢ Member Departmental Academic Review Committee
  - ➢ Member Board of Studies
  - ➢ Member Departmental Advisory Committee
- **CIIT Wah Cantt (Assistant Professor, July 2008 – May 2012)**
  - ➢ Taught at undergraduate and graduate levels
  - ➢ Supervised graduate theses
  - ➢ Acted as Program In charge for Bachelor of Science in Electrical (Computer) Engineering
  - ➢ Chairman Technical Evaluation Committee
  - ➢ Convener Program Team for Self-Assessment Report
  - ➢ Member Departmental Academic Review Committee
- **Institute of Microelectronics, GUCAS Beijing China (Feb 2005- June 2008)**
  - ➢ PhD Scholar (On Study Leave from COMSATS)
  - ➢ Research on the Design and Implementation of VLIW DSP Architectures
  - ➢ Research on Design Modeling and Performance Evaluation of Network on Chip Architectures and Algorithms
- **CIIT Wah Cantt (Lecturer, July 2003 – Feb 2005)**
  - ➢ Taught to Bachelor and Masters classes
  - ➢ Established Electronics and Microprocessor Labs
- **CIIT Wah Cantt (Research Associate, Sep 2001 – June 2003)**
  - ➢ Taught to Bachelor classes
  - ➢ Electronics Lab In-charge
- **BIIT Rawalpindi (Electronics Lab In-charge, March 1999 – Aug 2001)**
  - ➢ Taught to Bachelor Classes
  - ➢ Electronics Lab In-charge

Courses Taught

Graduate Courses	
Advanced Machine Learning	Advanced Digital Design
Advance Topics in Parallel Architectures	Advance Computer Architecture
Special Topics in Performance Evaluation of Networks	Advance Digital Signal Processing
Undergraduate Courses	
Machine Learning	Microprocessors and Assembly Language
Computer Architecture	Digital System Design
Computer Networks	Digital Logic Design
Signals and Systems	Electric Circuit Analysis
Electronics	Digital Signal Processing

Computer Skills

Programming Languages	
Python	Verilog HDL, System Verilog
System C	Assembly Language for Intel Computers
C/C++	Java
Engineering Tools	
NoCTweak	Nirgam
Orion 3.0	Reliablenoc
ModelSIM	Xilinx ISE
CINSIM	MATLAB, OCTAVE
NS-2	Electronic Work Bench
Operating Systems	
Windows X	Red Hat Linux
Ubuntu	Fedora

List of Publications

1. Usman Ali Gulzari , Waqar Farooq, Syed Nasir Mehmood Shah, Naveed Ahmad, Hessam Sarjoughian, Iftikhar Ahmed Khan, Sheraz Anjum, "The Scalable Octagonal-Cross-By-Pass-Torus topology for the on-chip-communication", Computer Networks available online 28 February 2025, https://doi.org/10.1016/j.comnet.2025.111178. **IF = 4.4**

2. Usman Ali Gulzari, Waqar Farooq, Syed Nasir Mehmood Shah, Iftikhar Ahmed Khan, Sheraz Anjum, Zoran Salcic, Hessam Sarjoughian "The Octagonal-Cross-By-Pass-Mesh Topology Design for the On-Chip-Communication", Computer Networks available online 28 November 2024, https://doi.org/10.1016/j.comnet.2024.110933. **IF = 5.84**

3. Usman Ali Gulzari, Zoran Salcic, Waqar Farooq, Sheraz Anjum, Sarzamin Khan, Muhammad Sajid, Frank Sill Torres, "Comparative Analysis of 2D Mesh Topologies with Additional Communication Links for On-Chip Networks", Computer Networks available online 29 January 2024, https://doi.org/10.1016/j.comnet.2024.110193. **IF = 5.493**

4. Basharat Hussain, Muhammad Khalil Afzal, Sheraz Anjum, Imran Rao, Byung-Seo Kim, "A Novel Graph Convolutional Gated Recurrent Unit Framework for Network-Based Traffic Prediction", IEEE Access November 2023, Volume 11, Pages 130102-130118, DOI: 10.1109/ACCESS.2023.3333938. **IF = 3.476**

5. Waqar Amin, Fawad Hussain, **Sheraz Anjum**, Sharoon Saleem, Naveed Khan Baloch, Yousaf Bin Zikria and Heejung Yu, "Efficient application mapping approach based on grey wolf optimization for network on chip" Journal of Network and Computer Applications October 2023, Volume 219, DOI: https://doi.org/10.1016/j.jnca.2023.103729. **IF = 8.7**

6. Waqar Amin, Fawad Hussain, **Sheraz Anjum**, Sharoon Saleem, Waqar Ahmad and Mubashir Hussain, "HyDra: Hybrid Task Mapping Application Framework for NOC-based MPSoCs" IEEE Access 24 May 2023, Volume 11, Page(s): 52309 – 52326, DOI: 10.1109/ACCESS.2023.3279501. **IF = 3.476**

7. Sarib Malik, Javeria Amin, Muhammad Sharif, Mussarat Yasmin, Seifedine Kadry, and **Sheraz Anjum**, "Fractured Elbow X-ray Image Classification Using Hand-Crafted and Deep Feature Fusion and Selection Based on Whale Optimization Approach" Mathematics 10th September 2022. **IF = 2.592**

8. Muhammad Ateeq, Muhammad Khalil Afzal, **Sheraz Anjum** and Byung-Seo Kim, "Cognitive Quality of Service Predictions in Multi-Node Wireless Sensor Networks", Computer Communications July 2022. https://doi.org/10.1016/j.comcom.2022.06.042. **IF = 5.047**

9. Waqar Amin, Fawad Hussain and **Sheraz Anjum**, "iHPSA: An improved bio-inspired hybrid optimization algorithm for task mapping in Network on Chip", Microprocessors and Microsystems (April 2022), Volume 90, https://doi.org/10.1016/j.micpro.2022.104493. **IF = 1.525**

10. Zulqar Nain, Rashid Ali, **Sheraz Anjum**, Muhammad Khalil Afzal and Sung Won Kim**, "**A Network Adaptive Fault-Tolerant Routing Algorithm for Demanding Latency and Throughput Applications of Network-on-a-Chip Designs**",** Electronics 2020, 9, 1076; doi:10.3390/electronics9071076. **IF = 2.412**

11. Waqar Amin, Fawad Hussain, **Sheraz Anjum**, Sarzamin Khan, Naveed Khan Baloch, Zulqar Nain, Sung Won Kim, "Performance evaluation of application mapping approaches for Network-on-Chip designs", IEEE Access, March 2020, Vol. 8: 63607-63631. DOI: 10.1109/ACCESS.2020.2982675, Electronic ISSN: 2169-3536. **IF = 4.640**

12. Ibrahim M, Baloch NK, **Anjum S**, Zikria YB, Kim SW. "An energy efficient and low overhead fault mitigation technique for internet of thing edge devices reliable on-chip communication", Wiley Software: Practice and Experience. 2020;1–18. https://doi.org/10.1002/spe.2796. **IF = 1.931**

13. Gulzari UA, Khan S, Sajid M, **Anjum S**, Torres FS, Sarjoughian H, et al., "A low latency and low power indirect topology for on-chip communication", PLoS ONE, October 2019, 14(10): e0222759. https://doi.org/10.1371/journal.pone.0222759. **IF=2.776**

14. Sarzamin Khan, Ayaz Ahmad, **Sheraz Anjum**, Tariq Umer and Usman Ali Gulzari, "An

Enhanced Simulation Framework for the Performance Evaluation of On-Chip Network Designs", The 9th IEEE Annual Information Technology, Electronics, and Mobile Communication Conference University of British Columbia, Vancouver, Canada 1st-3rd November 2018.

15. Sarzamin Khan, **Sheraz Anjum**, Usman Ali Gulzari, Farruh Ishmanov, Maurizio Palesi and Muhammad Khalil Afzal, "An Optimized Hybrid Algorithm in term of Energy and Performance for Mapping Real Time Workloads on 2D based On-Chip Networks", Applied Intelligence, December 2018, 48(12): 4792–4804. DOI 10.1007/s10489-018-1246-7. **IF 2.882**

16. Sarzamin Khan, **Sheraz Anjum**, Usman Ali Gulzari, Muhammad Khalil Afzal, Tariq Umer and Farruh Ishmanov, "An Efficient Algorithm for Mapping Real Time Embedded Applications on NoC Architecture", IEEE Access, March 2018, 6(1): 16324-16335. **DOI:** 10.1109/ACCESS.2018.2811716, Online ISSN: 2169-3536. **IF 4.098**

17. Aaqib Khalid, Tariq Umer, Muhammad Khalil Afzal, **Sheraz Anjum**, Adnan Sohail, Hafiz Muhammad Asif, "Autonomous Data Driven Surveillance and Rectification System using In-Vehicle Sensors for Intelligent Transportation Systems (ITS)" Elsevier Computer Networks, April 2018, Vol. 139: 109-118. doi.org/10.1016/j.comnet.2018.04.008. **IF 3.030**

18. Sarzamin Khan, **Sheraz Anjum**, Usman Ali Gulzari, Tariq Umer, Byung-Seo Kim, "Bandwidth-Constrained Multi-Objective Segmented Brute-Force Algorithm for Efficient Mapping of Embedded Applications on NoC Architecture", IEEE Access, November 2017, Vol. 6: 11242-11254. DOI: 10.1109/ACCESS.2017.2778340, Electronic ISSN: 2169-3536. **IF 3.557**

19. Sarzamin Khan, **Sheraz Anjum**, Usman Ali Gulzari, Frank Sill Torres, "Comparative Analysis of Network-on-Chip Simulation Tools", IET Computers & Digital Techniques, September 2017, 12(1): 30-38.DOI: 10.1049/iet-cdt.2017.0068, ISSN 1751-8601, Online ISSN 1751-861X. **IF 0.639**

20. Usman Ali Gulzari, **Sheraz Anjum**, Shahrukh Aghaa, Sarzamin Khan, Frank Sill , "An Efficient and Scalable Cross-By-Pass-Mesh topology for Networks on Chip" IET Computers & Digital Techniques, February 2017, 11(4): 140-148. **IF 0.639**

21. Gulzari UA, Sajid M, **Anjum S**, Agha S, Torres FS, "A New Cross-By-Pass-Torus Architecture Based on CBP-Mesh and Torus Interconnection for On-Chip Communication" PLoS ONE 11(12): e0167590. doi:10.1371/journal.pone.0167590, December 2016. **IF 2.806**

22. Usman Ali Gulzari, **Sheraz Anjum** and Shahrukh Agha, "Cross by Pass-Mesh Architecture for On-chip Communication", IEEE 9th International Symposium on Embedded Multicore/Many-core Systems-on-Chip (MCSoC), Turin, Italy 23-25 Sept. 2015: 267-274

23. Ehsan Munir, Saima Ijaz, **Sheraz Anjum**, Ali Khan, Waqas Anwar and Wasif Nisar, " Novel Approaches for Scheduling Task Graphs in Heterogeneous Distributed Computing Environment", The International Arab Journal of Information Technology, May 2015, 12(03): 270-277. **IF 0.519**

24. **Sheraz Anjum**, Ehsan Ullah Munir, Waqas Anwar and Nadeem Javaid, "Object Oriented Model for Evaluation of On-Chip Networks", Research Journal of Applied Sciences, Engineering and Technology, January 2013, 5(02): 353-356. **SJR 0.147**
https://www.scimagojr.com/journalsearch.php?q=19700187706&tip=sid&clean=0

25. Gulzari UA, **Anjum S**, Agha S., "2-Dimensional Router Design for Mesh On-Chip Communication", International Conference on Modeling and Simulation (ICOMS-2013).

26. **Sheraz Anjum**, Imran Ali Khan, Waqas Anwar, Ehsan Ullah Munir and Babar Nazir, "A Scalable and Minimized Butterfly Fat Tree (SMBFT) Switching Network for On-Chip Communication", Research Journal of Applied Sciences, Engineering and Technology, July 2012, 4(13): 1997-2002. **SJR 0.135**
https://www.scimagojr.com/journalsearch.php?q=19700187706&tip=sid&clean=0

27. Ehsan Ullah Munir, **Sheraz Anjum** , Muhammad Wasif Nisar , Waqas Anwar and Kashif Ayyub, "Comparative Study on Performance of Scheduling Heuristics in Heterogeneous Computing Environment", Pakistan Journal of Life and Social Sciences, 2011, 9(1): 70-79. (**ISI**

indexed)

28. **Sheraz Anjum**, Jie Chen, Pei-Pei Yue, and Jian Liu, "A Delay Optimized Architecture for On-Chip Communication", Journal of Electronic Science and Technology, June 2009, 7(2): 104-109. (ISI indexed through CSCD*) **CIF 0.142**
http://cstm.cnki.net/stmt/TitleBrowse/Detail?pykm=ZGKE&dbcode=STMJ

29. YUE Pei-pei, CHEN Jie, LIU Jian, **Sheraz Anjum**, "The Network Interface Design for Networks-on-Chip", Computer Engineering, June 2009, 35(10). (**ISI** indexed through CSCD*)

30. YUE Pei-pei, CHEN Jie, LIU Jian, Sheraz Anjum, "Fault-Tolerant Schemes for Networks-on-Chip", Microcomputer Information ISSN: 1008-0570, 2009, 25(10).

31. Yue Pei-pei, Chen Jie, Liu Jian, **Sheraz Anjum**, "Two-Channel Router Architecture for Networks-on-Chip", Journal of University of Electronic Science and Technology of China, 2009, 38(2): 309 - 312. (**ISI** indexed through CSCD*)

32. **Sheraz Anjum**, Chen Jie, Yue Pei-pei, and Liu Jian, "Traffic Modeling and Mapping of H.264 Encoder on 2D-Mesh Vs Application Specific NoC", Journal of System Simulation, May 2008, 20(10): 2782-2788. (**ISI** indexed through CSCD*) **(IF 0.914 in 2007 as per journal)**
http://www.china-simulation.com/English/2009-07-27/138.html

33. **Sheraz Anjum**, Chen Jie, Han Liang et al.., "A Scalable and Low Power VLIW DSP Core for Embedded System Design", Journal of Harbin Institute of Technology (New Series), April 2008, 15(2): 172-175. (**ISI** indexed through CSCD*) **SJR 0.139**
https://www.scimagojr.com/journalsearch.php?q=12658&tip=sid

34. Yue Pei-pei, LIU Jian, **SHEIKH Anjum**, CHEN Jie, "Enumeration-Based Path Allocation Algorithm in NoC Mapping [J]", Journal of University of Electronic Science and Technology of China, 2008, 37(1): 54-57. (**ISI** indexed through CSCD*)

35. Yue Pei-pei, LIU Jian, **Sheraz Anjum**, CHEN Jie, "A GA-based Path Allocation Algorithm in NoC", Microelectronics & Computer , 2008, 25(4). (**ISI** indexed through CSCD*)

36. **Sheraz Anjum**, Chen Jie and Li Hai Jun, "Design and Implementation of a Delay Optimized Multiply-Accumulate Unit for High Speed DSPs", Chinese Journal of Electron Devices, Aug 2007, 30(4): 1375-1379. (**ISI** indexed through INSPEC) **SJR 0.102**
https://www.scimagojr.com/journalsearch.php?q=24285&tip=sid&clean=0

37. **Sheraz Anjum** and Chen Jie, "An Efficient Architecture of Instruction Cache Unit (ICU) with Emphasis on its Performance Analysis", Chinese Journal of Electron Devices, Oct. 2007, 30(5): 1861-1865. (**ISI** indexed through INSPEC) **SJR 0.102**
https://www.scimagojr.com/journalsearch.php?q=24285&tip=sid&clean=0

38. Li Jin Hai, Ba Xiao Hui, **Sheraz Anjum** and Chen Jie, "An Adaptive Algorithm for Fast Acquisition of GPS Signals", Chinese Journal of Electron Devices, June 2007, 30(3): 1024-1027. (**ISI** indexed through INSPEC) **SJR 0.102**
https://www.scimagojr.com/journalsearch.php?q=24285&tip=sid&clean=0

39. Li Jin Hai, Ba Xiao Hui, **Sheraz Anjum** and Chen Jie, "Adaptive PLL Based Carrier Tracking Algorithm for GPS Signals in High Dynamic Environments", Chinese Journal of Electron Devices, Aug 2007, 30(4): 1440-1443. (**ISI** indexed through INSPEC) **SJR 0.102**
https://www.scimagojr.com/journalsearch.php?q=24285&tip=sid&clean=0

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*CSCD stands for Chinese Science Citation Database